MODEL NO. : TM033XDHG01-00

ISSUED DATE: 2016-11-21

VERSION : Ver 1.0

■Preliminary Specification
□Final Product Specification

Customer :

Approved by	Notes

SHANGHAI TIANMA Confirmed :

Prepared by	Checked by	Approved by

This technical specification is subjected to change without notice

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Record of Revision

Rev	Issued Date	Description	Editor
1.0	2016-11-21	Preliminary spec release	Jin Zhao

1 General Specifications

Feature		Spec
Display Spec.	Size	3.34inch
	Resolution	320(RGB) X 320
	Interface	MIPI (Command Mode)
	Color Depth	16M dithering
	Technology Type	a-si TFT
	Pixel pitch(mm)	0.1875 x 0.1875
	Pixel Configuration	R.G.B. Vertical Stripe
	Display Mode	TM with Normally White
	Surface Treatment	HC
	Viewing Direction	12 o'clock
	Gray Scale Inversion Direction	6 o'clock
Mechanical Characteristics	LCM (W x H x D) (mm)	66.20×69.30×2.66 (Not include FPC dimension)
	Active Area(mm)	Diameter 60mm
	Driver IC	ST7796SI
	Weight (g)	TBD
	LED Numbers	6 LEDs Serial

Note 1: Requirements on Environmental Protection: Q/S0002

Note 2: LCM weight tolerance: ± 5%

2 Input/Output Terminals

2.1 TFT LCD Panel

No	Symbol	I/O	Description
1	GND	P	Ground
2	RST	I	Global reset signal
3	GND	P	Ground
4	MIPI_Data-N	I	MIPI Clock
5	MIPI_Data-P	I	MIPI Clock
6	GND	P	Ground
7	MIPI_CLK-N	I	MIPI Data
8	MIPI_CLK-P	I	MIPI Data
9	GND	P	Ground
10	TE	O	Tearing effect output
11	GND	P	Ground
12	VDD	P	Analog power supply
13	VDDIO	P	Digital power supply
14	GND	P	Ground
15	LEDA	P	LED Anode
16	LEDK	P	LED Cathode
17	GND	P	Ground

Note 1: I——Input，O——Output，P——Power/Ground,VCC=VDD

Table 2.1 Terminal pin assignments

3 Absolute Maximum Ratings

3.1 Driving TFT LCD Panel

Ta =25℃

Item	Symbol	MIN	MAX	Unit	Remark
Power Supply Voltage	VDD	-0.3	4	V	
Back Light Forward Current	ILED		25	mA	One LED
Operating Temperature	T_{OPR}	-20	70	℃	
Storage Temperature	T_{STG}	-30	80	℃	
Relative Humidity Note2	RH	--	≤95	%	Ta≤40℃
		--	≤85	%	40℃＜Ta≤50℃
		--	≤55	%	50℃＜Ta≤60℃
		--	≤36	%	60℃＜Ta≤70℃
		--	≤24	%	70℃＜Ta≤80℃
Absolute Humidity	AH	--	≤70	g/m^3	Ta＞70℃

Table 3.1 absolute maximum rating

Notes:
1. In case of below 0℃，the response time of liquid crystal (LC)becomes slower .
2. The color of panel becomes darker than normal one. Level of retardation depends on temperature, because of LC's characteristics.
3. If the module is above these absolute maximum ratings. It may become permanently damaged. Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.

4 Electrical Characteristics

4.1 Driving TFT LCD Panel

GND=0V，Ta=25℃

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Supply Voltage		VDD	2.5	2.8	3.6	V	
IO Supply Voltage		VDDI	1.65	2.8	VDD	V	Connect to VDD internal.
Input Signal Voltage	Low Level	VIL	DGND	—	0.3 VDDI	V	
	High Level	VIH	0.7 VDDI	—	VDDI	V	
Output Signal Voltage	Low Level	VOL	DGND	—	0.2VDDI	V	
	High Level	VOH	0.8VDDI	—	VDDI	V	
(Panel+LSI) Power Consumption	Black Mode (60Hz)					mA	Note 1,3,4
	Sleep Mode					mA	Note 3,4

Table 4.1 LCD module electrical characteristics

Notes:
1. All pixels black.
2. All pixels white.
3. Typical: VDDI=1.8V, VDD=2.8V; Maximum: VDDI=3.3V, VDD=3.3V
4. The power consumption was only reference

4.2 Backlight Unit

Ta=25℃

Item	Symbol	Min	Typ	Max	Unit	Remark
Forward Current	I_F	18	20	25	mA	6 LEDs (6 LED Serial,)
Forward Voltage	V_F	16.8	18.0	19.2	V	
Operating Life Time	-	20,000	-	-	Hrs	

Note1: The LED driving condition is defined for each LED module (6 LED Serial,).For each LED: I_F =20mA, V_F (1/6) =3.2V.

Note2: Under LCM operating, the stable forward current should be inputted. And forward voltage is for reference only.

Note3: I_F is defined for one channel LED. Optical performance should be evaluated at Ta=25℃ only If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED   will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

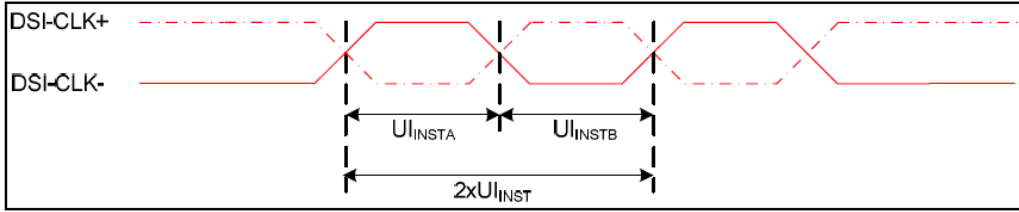Note4: The LED driving condition is defined for each LED

4.3 Block Diagram

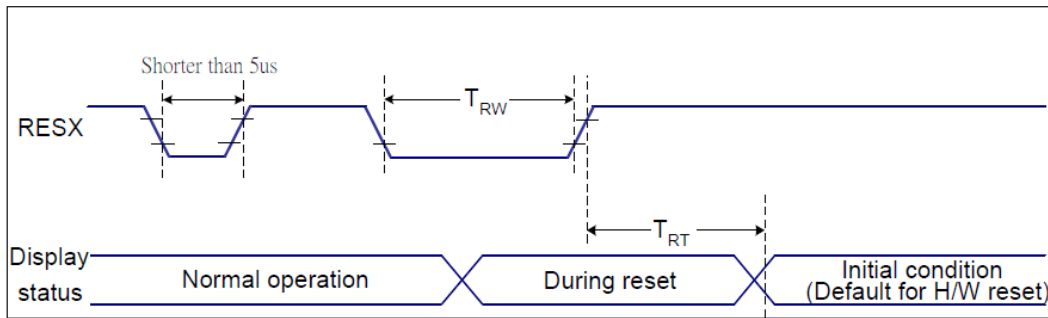### LCD module diagram
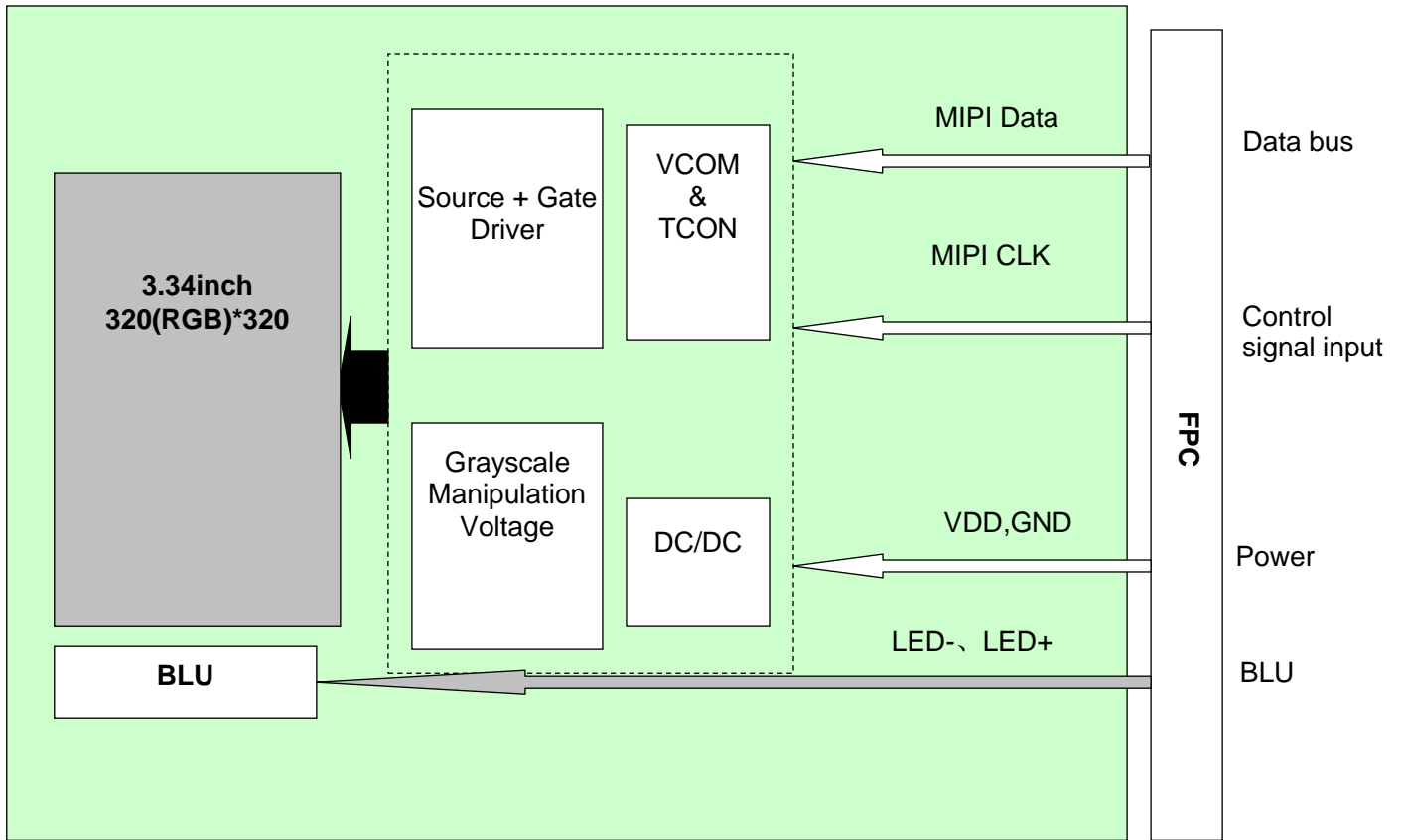
5 Timing Chart

5.1 INTERFACE TIMING

High Speed Mode – Clock Channel Timing

VDDI=1.8V,VDDA=2.8V, AGND=DGND=0V, Ta=25 ℃

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
DSI-DATA_P/N	2xUI INST	Double UI instantaneous	4	25	ns	
DSI-DATA_P/N	UI INSTA ,UI INSTB	UI instantaneous Half	2	12.5	ns	

5.2 Reset Timing

Reset Timing

VDDI=1.8V,VDDA=2.8V, AGND=DGND=0V, Ta=25℃

| Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	us
	TRT	Reset cancel	-	5 (Note 1, 5)	ms
				120 (Note 1, 6, 7)	ms

Table 1 Reset Timing

Notes:
1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from NVM (or similar device) to
registers. This loading is done every time when there is HW reset cancel time (tRT) within 5 ms after a rising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the table below:

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120
ms, when Reset Starts in Sleep Out –mode. The display remains the blank state in Sleep In –mode.) and then return to Default condition
for Hardware Reset.
4. Spike Rejection also applies during a valid reset pulse as shown below:
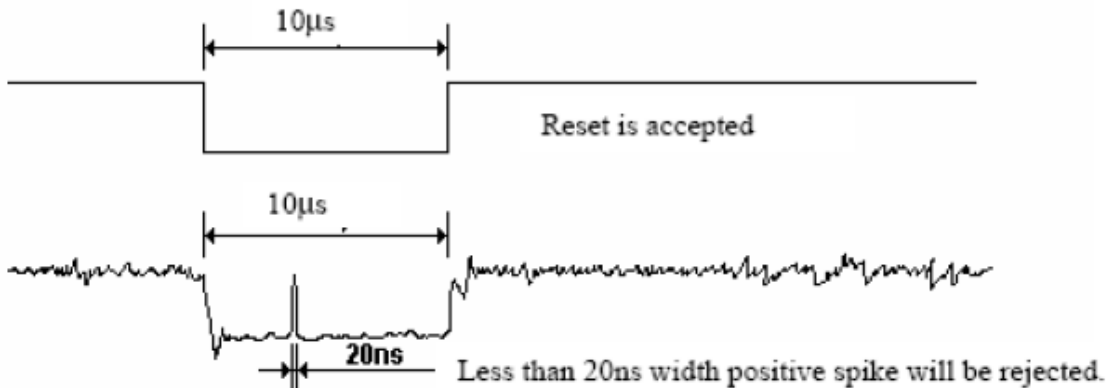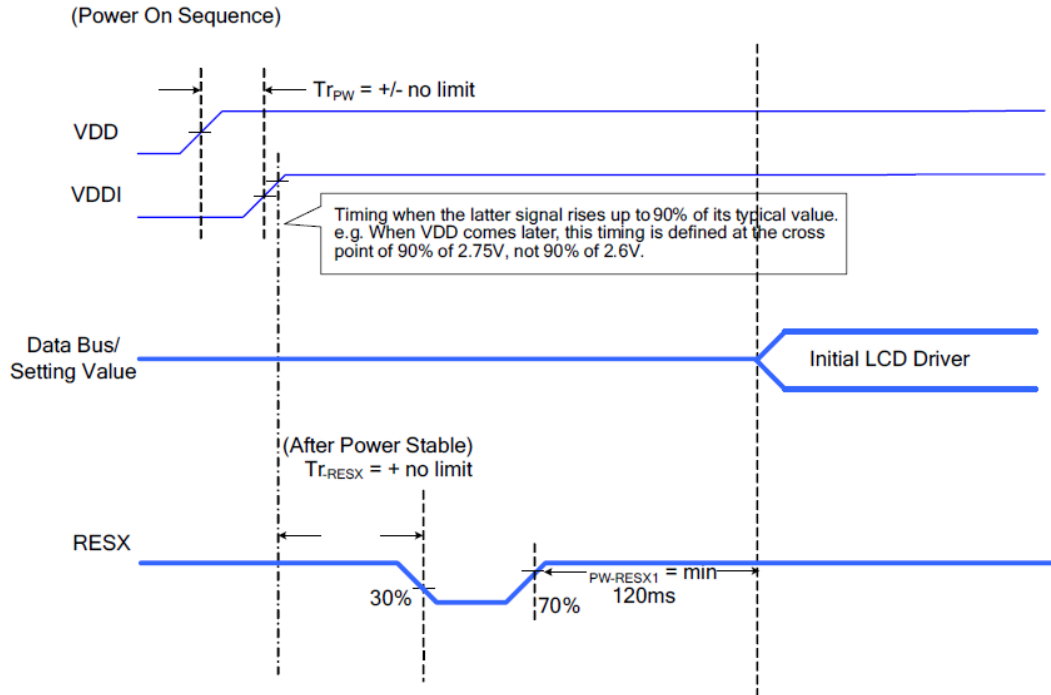

5. When Reset applied during Sleep In Mode.
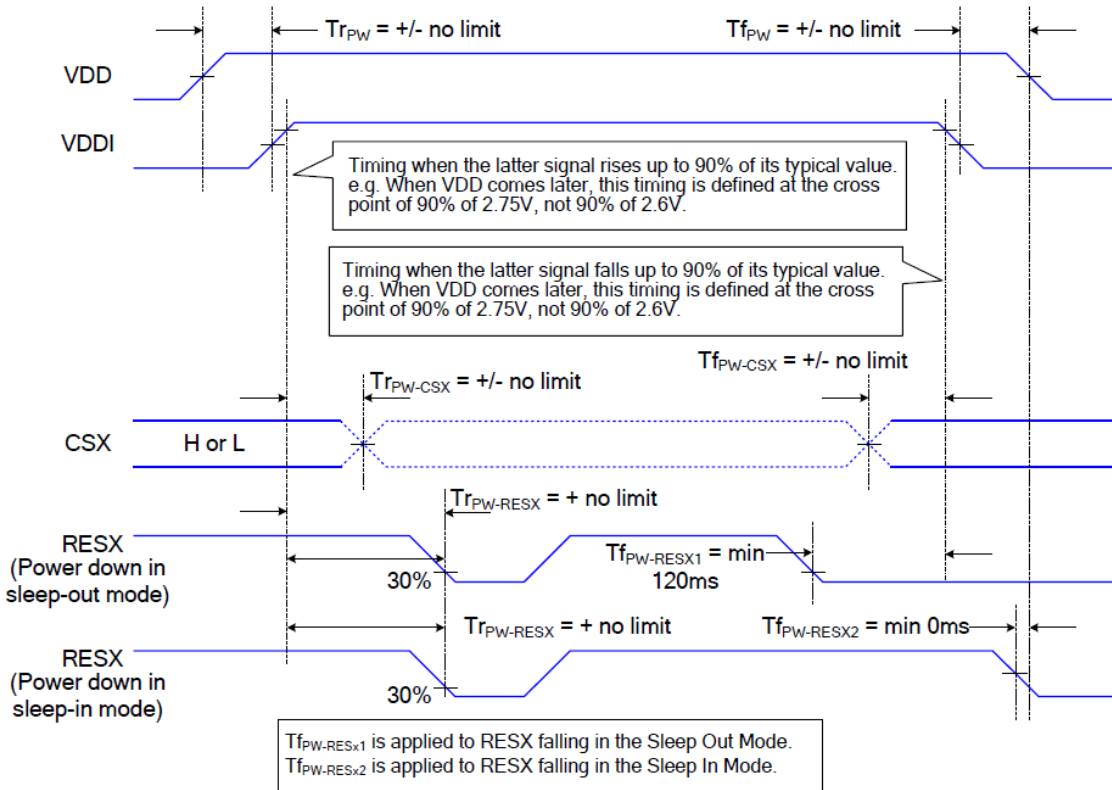*6. When Reset applied during Sleep Out Mode.*
7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for
120msec.

5. 3 Power-On Sequence

The power on sequence:

(Power On Sequence)

$Tr_{PW} = +/-$ no limit

VDD

VDDI

Timing when the latter signal rises up to 90% of its typical value.
e.g. When VDD comes later, this timing is defined at the cross
point of 90% of 2.75V, not 90% of 2.6V.

Data Bus/
Setting Value

Initial LCD Driver

(After Power Stable)
$Tr._{RESX} = +$ no limit

RESX

30%

70%

$PW-RESX1 = min$
120ms

5. 4 Power-Off Sequence

$Tr_{PW} = +/-$ no limit

$Tf_{PW} = +/-$ no limit

VDD

VDDI

Timing when the latter signal rises up to 90% of its typical value.
e.g. When VDD comes later, this timing is defined at the cross
point of 90% of 2.75V, not 90% of 2.6V.

Timing when the latter signal falls up to 90% of its typical value.
e.g. When VDD comes later, this timing is defined at the cross
point of 90% of 2.75V, not 90% of 2.6V.

$Tf_{PW-CSX} = +/-$ no limit

$Tr_{PW-CSX} = +/-$ no limit

CSX   H or L

$Tr_{PW-RESX} = +$ no limit

RESX
(Power down in
sleep-out mode)

30%

$Tf_{PW-RESX1} = min$
120ms

$Tr_{PW-RESX} = +$ no limit

$Tf_{PW-RESX2} = min$ 0ms

RESX
(Power down in
sleep-in mode)

30%

$Tf_{PW-RESx1}$ is applied to RESX falling in the Sleep Out Mode.
$Tf_{PW-RESx2}$ is applied to RESX falling in the Sleep In Mode.

Note: Please refer to ST7796SI data sheet for more details.ST7796SI INTERFACE PROTOCOL

6 Optical Characteristics

Ta=25℃

Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		θT	CR≥10	60	70	-	Degree	Note2,3
		θB		70	80	-		
		θL		70	80	-		
		θR		70	80	-		
Contrast Ratio		CR	θ=0°	600	800	-		Note 3
Response Time		T_{ON}	25℃	-	20	30	ms	Note 4
		T_{OFF}						
Chromaticity	White	x	Backlight is on	0.235	0.285	0.335	-	Note 1,5
		y		0.258	0.308	0.358		
	Red	x		0.535	0.585	0.635		Note 1,5
		y		0.274	0.324	0.374		
	Green	x		0.289	0.339	0.389		Note 1,5
		y		0.537	0.587	0.637		
	Blue	x		0.101	0.151	0.201		Note 1,5
		y		0.038	0.088	0.138		
Uniformity		U		70	75	-	%	Note 6
NTSC				45	50	-	%	Note 5
Luminance(without lens)		L		600	-	-	cd/m^2	Note 7

Test Conditions:

1. I_F= 20 mA, and the ambient temperature is 25℃.

2. The test systems refer to Note 1 and Note 2.

Note 1:   Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.
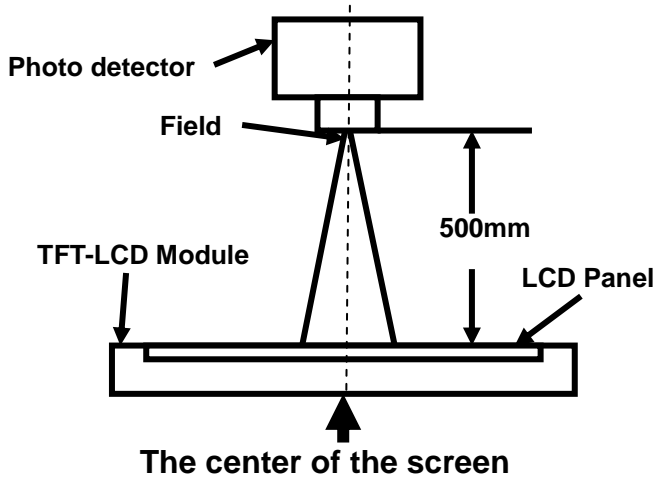
Note 2:   Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD by CONOSCOPE(ergo-80)。
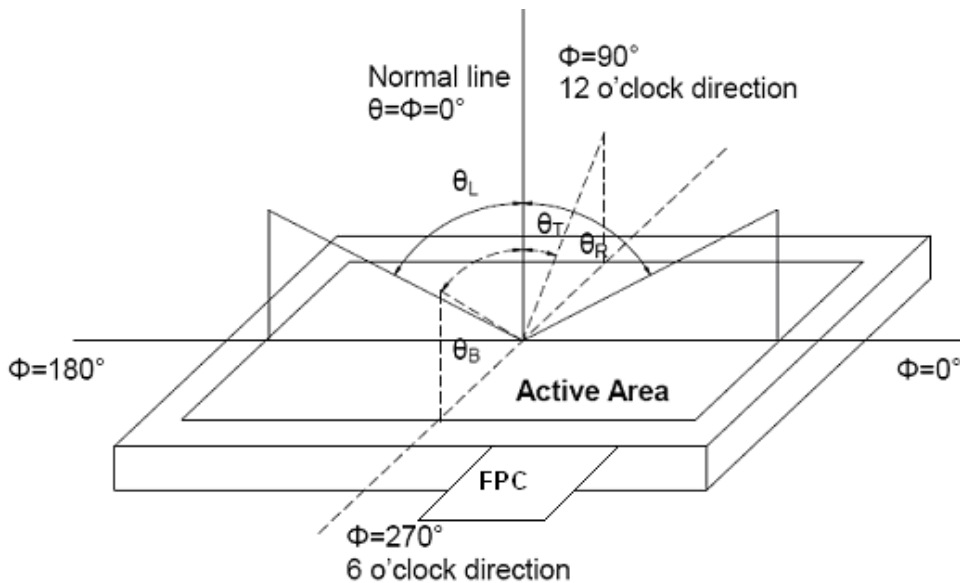
Note 3:   Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

"White state ": The state is that the LCD should drive by Vwhite.

"Black state": The state is that the LCD should drive by Vblack.

Vwhite: TBD V   Vblack: TBD V.

Note 4:   Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.
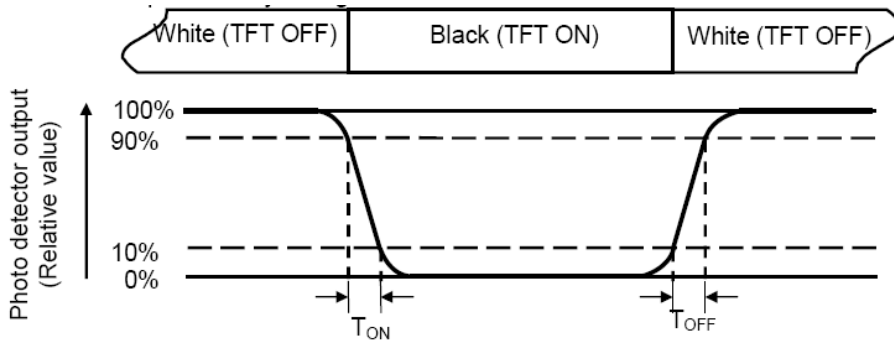
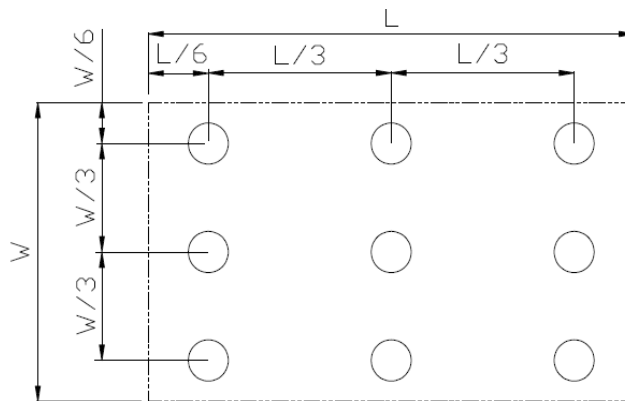Note 5:   Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6:   Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

   Luminance Uniformity (U) = Lmin/ Lmax

   L-------Active area length W----- Active area width

   Lmax: The measured Maximum luminance of all measurement position.

   Lmin: The measured Minimum luminance of all measurement position.

Note 7:   Definition of Luminance:

Measure the luminance of white state at center point.

7 Environmental / Reliability Tests

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts=+70℃, 160hrs	Note1 IEC60068-2-1,GB2423.2
2	Low Temperature Operation	Ta=-20℃, 160hrs	IEC60068-2-1 GB2423.1
3	High Temperature Storage	Ta=+80℃, 160hrs	IEC60068-2-1 GB2423.2
4	Low Temperature Storage	Ta=-30℃, 160hrs	IEC60068-2-1 GB2423.1
5	High Temperature & High Humidity Storage	Ta=+60℃, 90% RH 160 hours	Note2 IEC60068-2-78 GB/T2423.3
6	Thermal Shock (Non-operation)	-20℃ 30 min ~ +60℃ 30 min, Change time: 1min, 100 Cycles	Start with cold temperature, End with high temperature, IEC60068-2-14,GB2423.22
7	Electro Static Discharge (Operation)	C=150pF, R=330Ω，5points/panel Air:±4KV, 5times; Contact:±2KV, 5 times; （Environment: 15℃～35℃, 30%～60%, 86Kpa～106Kpa）	IEC61000-4-2 GB/T17626.2 Class B
8	Package Vibration Test	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total)(Package condition)	IEC60068-2-6 GB/T2423.10
9	Shock (Non-operation)	60G 6ms, ±X,±Y,±Z 3times, for each direction	IEC60068-2-27 GB/T2423.5
10	Package Drop Test	Height:60 cm, 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8

Note1: Ts is the temperature of panel's surface.

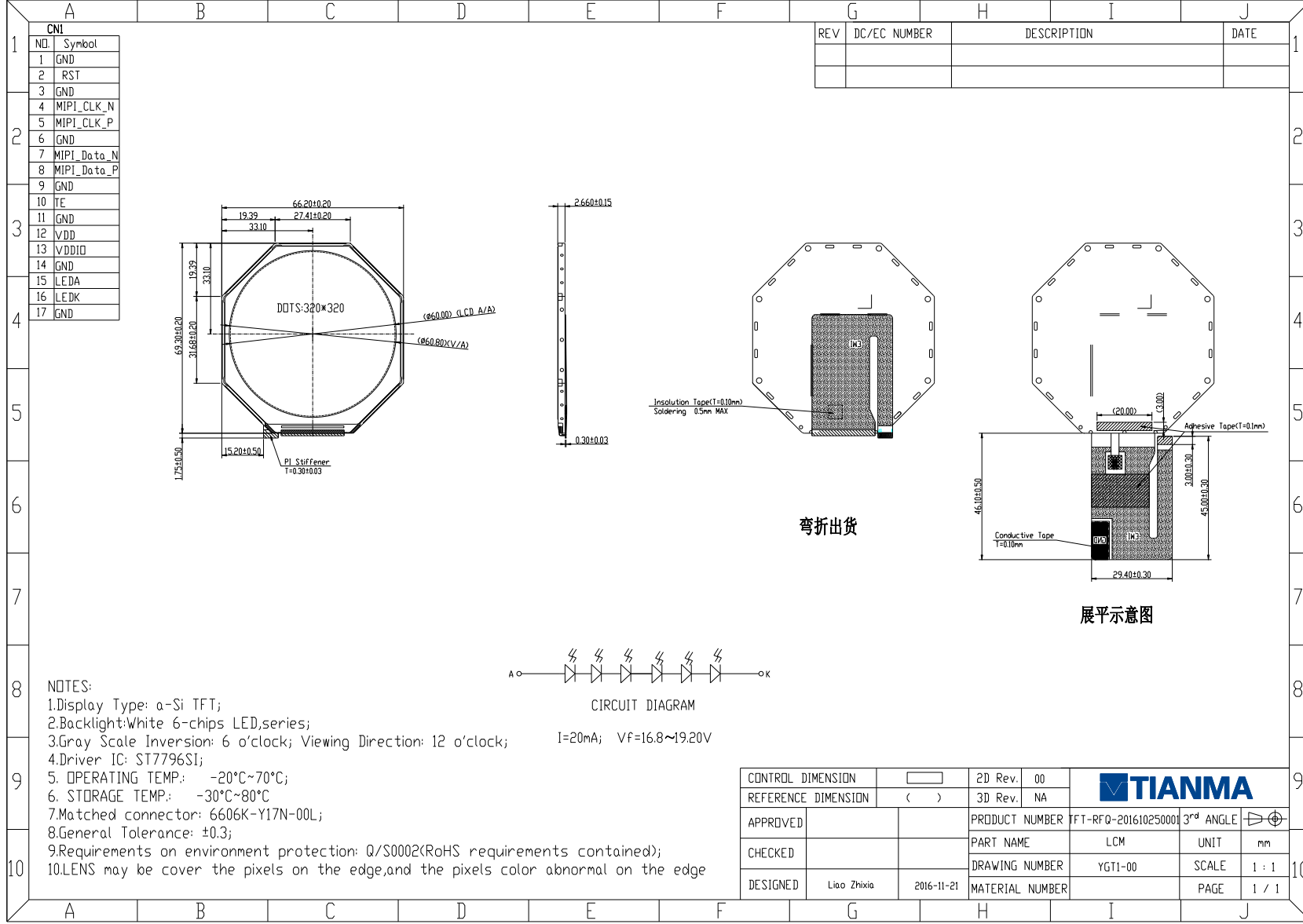Note2: Ta is the ambient temperature of sample.

Note3: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

Note 4: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.

TM033XDHG01-00

8 Mechanical Drawing

CN1

NO.	Symbol
1	GND
2	RST
3	GND
4	MIPI_CLK_N
5	MIPI_CLK_P
6	GND
7	MIPI_Data_N
8	MIPI_Data_P
9	GND
10	TE
11	GND
12	VDD
13	VDDIO
14	GND
15	LEDA
16	LEDK
17	GND

66.20±0.20
19.39
33.10
27.41±0.20
19.39
33.10
DOTS:320×320
69.30±0.20
31.68±0.20
(Φ60.00)(LCD A/A)
(Φ60.80)(V/A)
1.75±0.50
15.20±0.50
PI Stiffener T=0.30±0.03

2.660±0.15
0.30±0.03

Insolution Tape(T=0.10mm)
Soldering 0.5mm MAX

弯折出货

(20.00)
(3.00)
Adhesive Tape(T=0.1mm)
46.10±0.50
3.00±0.30
45.00±0.30
Conductive Tape T=0.10mm
29.40±0.30

展平示意图

NOTES:
1.Display Type: a-Si TFT;
2.Backlight:White 6-chips LED,series;
3.Gray Scale Inversion: 6 o'clock; Viewing Direction: 12 o'clock;
4.Driver IC: ST7796SI;
5. OPERATING TEMP.: -20℃~70℃;
6. STORAGE TEMP.: -30℃~80℃;
7.Matched connector: 6606K-Y17N-00L;
8.General Tolerance: ±0.3;
9.Requirements on environment protection: Q/S0002(RoHS requirements contained);
10.LENS may be cover the pixels on the edge,and the pixels color abnormal on the edge

CIRCUIT DIAGRAM
A ○—————————○ K
I=20mA; Vf=16.8~19.20V

REV	DC/EC NUMBER	DESCRIPTION	DATE

CONTROL DIMENSION		□	2D Rev.	00	**TIANMA**		
REFERENCE DIMENSION		( )	3D Rev.	NA			
APPROVED			PRODUCT NUMBER	TFT-RFQ-20161025000	3rd ANGLE	
CHECKED			PART NAME	LCM	UNIT	mm
DESIGNED	Liao Zhixia	2016-11-21	DRAWING NUMBER	YGT1-00	SCALE	1:1
			MATERIAL NUMBER		PAGE	1/1

9 Packing Drawing

No	Item	Model (Material)	Dimensions(mm)	Unit Weight(Kg)	Quantity	Remark
1	LCM module			TBD	126	
2	Tray	TM033XDZN01-00 YBZ1-00	251×351×13.5	TBD	24	
3	EPE1	MBZ-ZZML1	336×246×6mm	TBD	6	
4	EPE2	MBZ-ZZM15	375×275×10mm	TBD	4	
5	EPE3	MBZ-ZZM16	250×280×12mm	TBD	2	
6	Carton	X18A	395×290×315mm	TBD	1	
7	ES bag	JD13	400×520	TBD	3	
8	Total weight	TBD±5% Kg				

10 Precautions for Use of LCD Modules

10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaMinated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

－ Isopropyl alcohol

－ Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

－ Ketone

－ Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

    10.1.8.1 Be sure to ground the body when handling the LCD Modules.

    10.1.8.2 Tools required for assembly, such as soldering irons, must be properly ground.

    10.1.8.3 To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

    10.1.8.4 The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

10.2 Storage precautions

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃ Relatively humidity: ≤80%

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

10.3 Transportation Precautions

10.3.1 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.