

1. Three Major TFT LVDS Signal Mapping

		Channels Formats		
		18bit	24bit (VESA)	24bit(JEIDA)
LVDS Channels	RX0+/-	R0~R5,G0	R0~R5,G0	R2~R7,G2
	RX1+/-	G1~G5,B0,B1	G1~G5,B0~B1	G3~G7,B2~B3
	RX2+/-	B2~B5,HS,VS,DE	B2~B5,HS,VS,DE	B4~B7,HS,VS,DE
	RX3+/-	Not use	R6,R7,G6,G7,B6,B7	R0,R1,G0,G1,B0,B1

Note:

*1. For 18bit colors, R5, G5, B5 are the MSB of each color

*2. For 24bit colors, R7, G7, B7 are the MSB of each color

2. Host and TFT module Workable LVDS Signal matching:

		Host Output Format		
		18bit	24bit (VESA)	24bit(JEIDA)
TFT Input Format	18bit	O	X	O
	24bit (VESA)	X	O	X
	24bit (JEIDA)	O	X	O

3.

LMT080DIEFWU-AAN designed for support 18bit and 24bit(VESA), which could support and connected to most of the Host. Listed as follow

		Host Output Format		
		18bit	24bit (VESA)	24bit(JEIDA)
18bit	O	X	O	
24bit (VESA)	X	O	X	